WEST Search History

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DATE: Friday, November 25, 2005

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L150	(6931641 or 6658447 or 5327526).pn.	3
☐	L149	542189.ap.	4
☐	L148	L144 and L146	3
☐	L147	L32 and L146	13165
☐	L146	L89 or L100	72412
☐	L145	L41 and L144	1
☐	L144	L1 same L143	32
☐	L143	branch$3 with condition$4	24124
☐	L142	(L38 with branch$3 with condition$4) and L141	8
☐	L141	L89 or router$1	72412
☐	L140	6829697.pn.	1
☐	L139	L137 and L38	4
☐	L138	connect$3 with L137	41
☐	L137	logic blocks with series	186
☐	L136	L38 same L135	30
☐	L135	single pipeline	758
☐	L134	functional unit$1 with L38 with branch$3	7
☐	L133	pipeline stage$1 with L38 with branch$3	1
☐	L132	pipeline same conditional branch same packet$1	20
☐	L131	pipelined logic blocks	3
☐	L130	pipelined logic blocksL129	0
☐	L129	serial logic blocks	0
☐	L128	series same L126	12
☐	L127	L38 and L126	29
☐	L126	serial same L123	143
☐	L125	L38 with L123	8
☐	L124	L114 with L123	2
☐	L123	protocol processor$1	1345
		DB=USPT; PLUR=NO; OP=ADJ	
☐	L122	protocol processor$1	766
☐	L121	branch$3 same L120	1

☐	L120	L114 same packet$1 same (switch$3 or swap$4)	100
☐	L119	L114 and L52	13
☐	L118	L114 and L117	0
☐	L117	L65 with (serial or pipelin$3)	18
☐	L116	serial and L115	16
☐	L115	L113 with L114	34
☐	L114	multi-threading or multithreading or thread$1	241890
☐	L113	packet process$3	3899
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L112	L6 with L38	182
☐	L111	suspend$3 same L110	5
☐	L110	L6 same L38	243
☐	L109	L6 and L38	867
☐	L108	L100 same L106	3
☐	L107	L100 and L106	123
☐	L106	L38 with suspend$3	2428
☐	L105	L103 and L104	3
☐	L104	L1 and L101	320
☐	L103	packet$1 and L102	47
☐	L102	L38.ti,ab,clm. and L100.ti,ab,clm.	144
☐	L101	L38 and L100	5785
☐	L100	router$1	59140
☐	L99	L89 and L98	6
☐	L98	(712/23).ccls.	704
☐	L97	L65 and L94	1
☐	L96	L49 and L94	1
☐	L95	L89 and L94	4
☐	L94	(712/241).ccls.	254
☐	L93	L70 and L89	0
☐	L92	L70 and L91	0
☐	L91	L38 and L89	1280
☐	L90	L65 and L89	24
☐	L89	packet switch$3	21050
☐	L88	sun.as. and treat.xp. and L38	9
☐	L87	L38 same L86	37
☐	L86	instruction block$1	2021
☐	L85	L38 and L83	5
☐	L84	packet$1 and L83	13

☐	L83	decoupled processor$1	61
☐	L82	L81 adj processor$1	5
☐	L81	non-blocking or nonblocking	6923
☐	L80	mon-blocking or nonblocking	1286
☐	L79	L38 with L65	20
☐	L78	L38 adj processor	754
☐	L77	L65 and L70	1
☐	L76	L62 and L70	3
☐	L75	L70 and L74	1
☐	L74	(712/32).ccls.	373
☐	L73	context switch$3 same L59	33
☐	L72	L59 and L70	1
☐	L71	L49 and L70	0
☐	L70	(712/235).ccls.	188
☐	L69	packet$1.ti,ab,clm. and L68	9
☐	L68	L65 with (parallel or pipelin$3)	146
☐	L67	packet$1.ti,ab,clm. and L65	81
☐	L66	packet.ti,ab,clm. and L65	67
☐	L65	subprocessor$1 or sub-processor$1	1185
☐	L64	L38 same L62	30
☐	L63	packet$1 same L62	18
☐	L62	non-blocking with processor$1	348
☐	L61	L60 not L58	5
☐	L60	L59 and L53	14
☐	L59	packet with processor$1	13263
☐	L58	L49 and L53	9
☐	L57	packet$1 and L52	22
☐	L56	L52 and L55	8
☐	L55	conditional branch$3	5445
☐	L54	L52 and L53	0
☐	L53	(712/234).ccls.	352
☐	L52	serial processors	99
☐	L51	L38 with L49	30
☐	L50	L38 and L49	199
☐	L49	packet processor$1	1652
☐	L48	L47 not private branch exchange	36
☐	L47	L45 not L46	48

☐	L46	branch$3 same L44	5
☐	L45	branch$3 and L44	53
☐	L44	L38 same L41	253
☐	L43	branch$3 and L42	521
☐	L42	L38 and L41	2963
☐	L41	decoupled	29350
☐	L40	L38 and L39	8
☐	L39	decoupled architecture	34
☐	L38	multi-threading or multithreading or thread$1	305828
☐	L37	chained processor$1	8
☐	L36	L28 and L35	0
☐	L35	L34 with buffer$1	0
☐	L34	reconfigurable pipeline$1	100
☐	L33	L29 and L32	2
☐	L32	709/$.ccls.	40442
☐	L31	router$1 and L29	19
☐	L30	L28 and L29	2
☐	L29	reservation station$1	1048
☐	L28	internet protocol or tcp	73404
☐	L27	L25 and L26	18
☐	L26	reservation station$1 with branch$3	329
☐	L25	internet protocol or ip or tcp	136611
☐	L24	reservation station$1 and L23	10
☐	L23	(data or network) adj packet$1	50716
☐	L22	(router$1 or switch$2) and L21	83
☐	L21	packet$1 and reservation station$1	156
☐	L20	L18 same next instruction	18
☐	L19	pipeline stage$1 with conditional branch$3	111
☐	L18	pipeline stage$1 with branch$3	526
☐	L17	L16 not L10	64
☐	L16	stall$3 and L15	65
☐	L15	L6 same thread$1	235
☐	L14	L11 and L6	1
☐	L13	serial sub-processors	0
☐	L12	serial subprocessors	0
☐	L11	serial processors	99
☐	L10	L6 and L9	14

☐	L9	stall$3 same L1	89
☐	L8	stall$3 and L7	110
☐	L7	L1 and L6	250
☐	L6	packet process$3	8317
☐	L5	conditional branch$3 same L4	6
☐	L4	new thread	1711
☐	L3	conditional branch$3 same L1	20
☐	L2	conditional branch$3 with L1	2
☐	L1	context adj (switch$3 or swap$4)	4274

END OF SEARCH HISTORY